

**PATENT NUMBER**

U.S. UTILITY Patent Application

**O.P.E.**

PATENT DATE

SCANNED

## APPLICANTS

Enjanzhang

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High-speed source-synchronous signaling for interfacing VLSI CMOS circuits to transmission lines

PTO-2040  
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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>		
	Sheets Draw.	Figs. Draw.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		_____ (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>
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			Amount Due	Date Paid	
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(FACE)